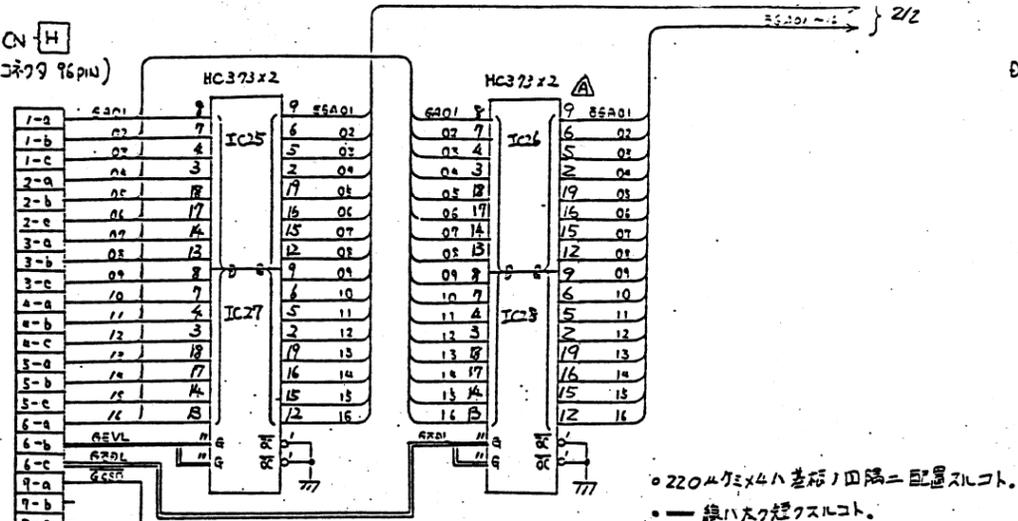
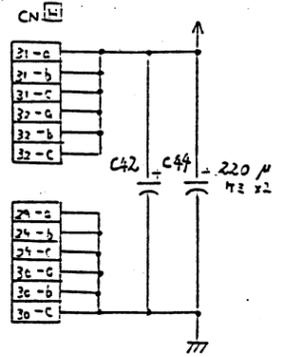
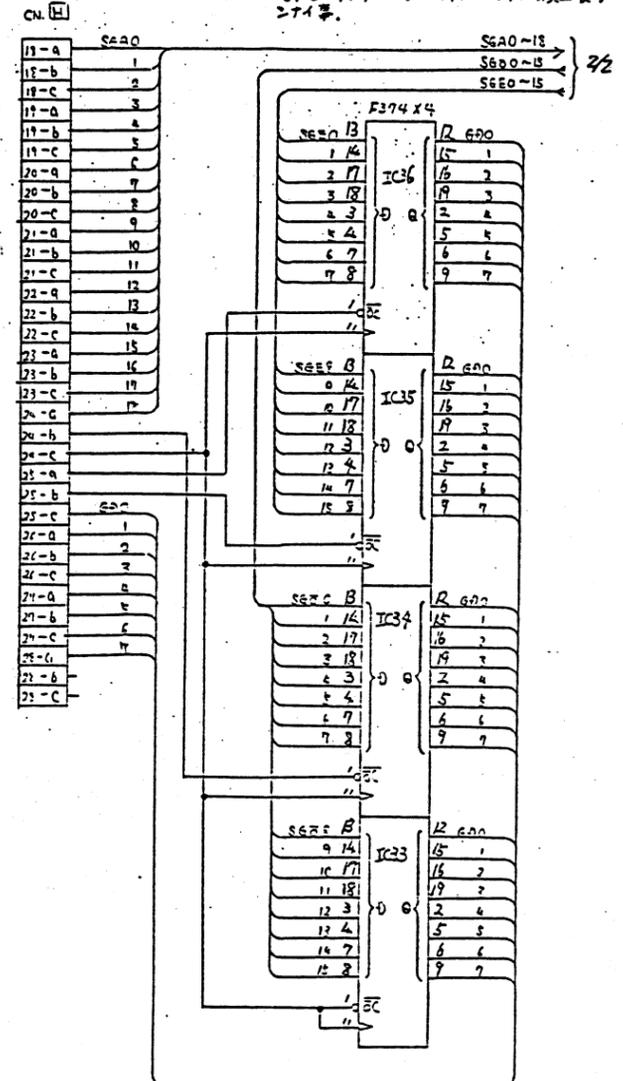
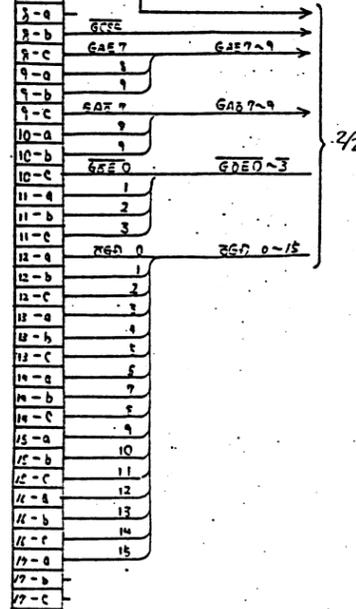


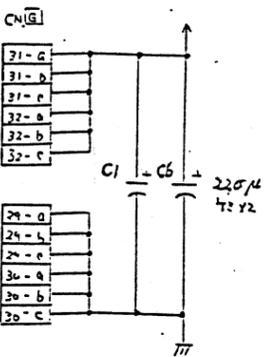
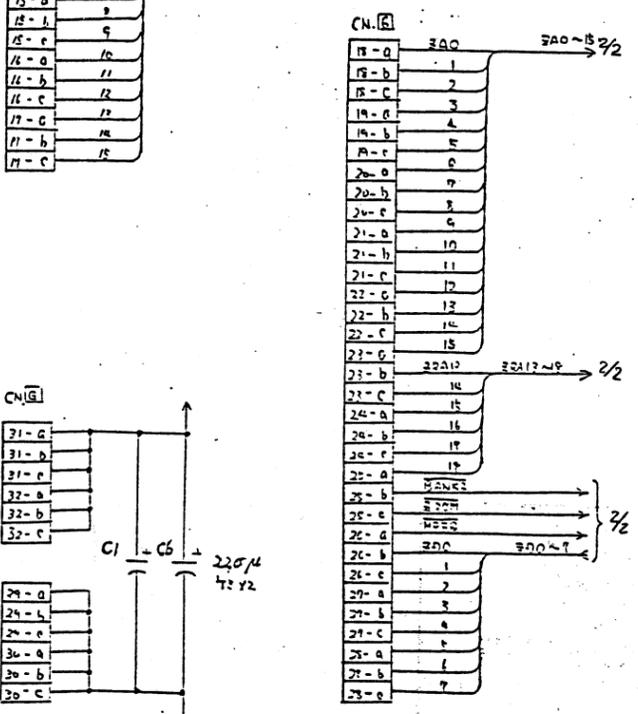
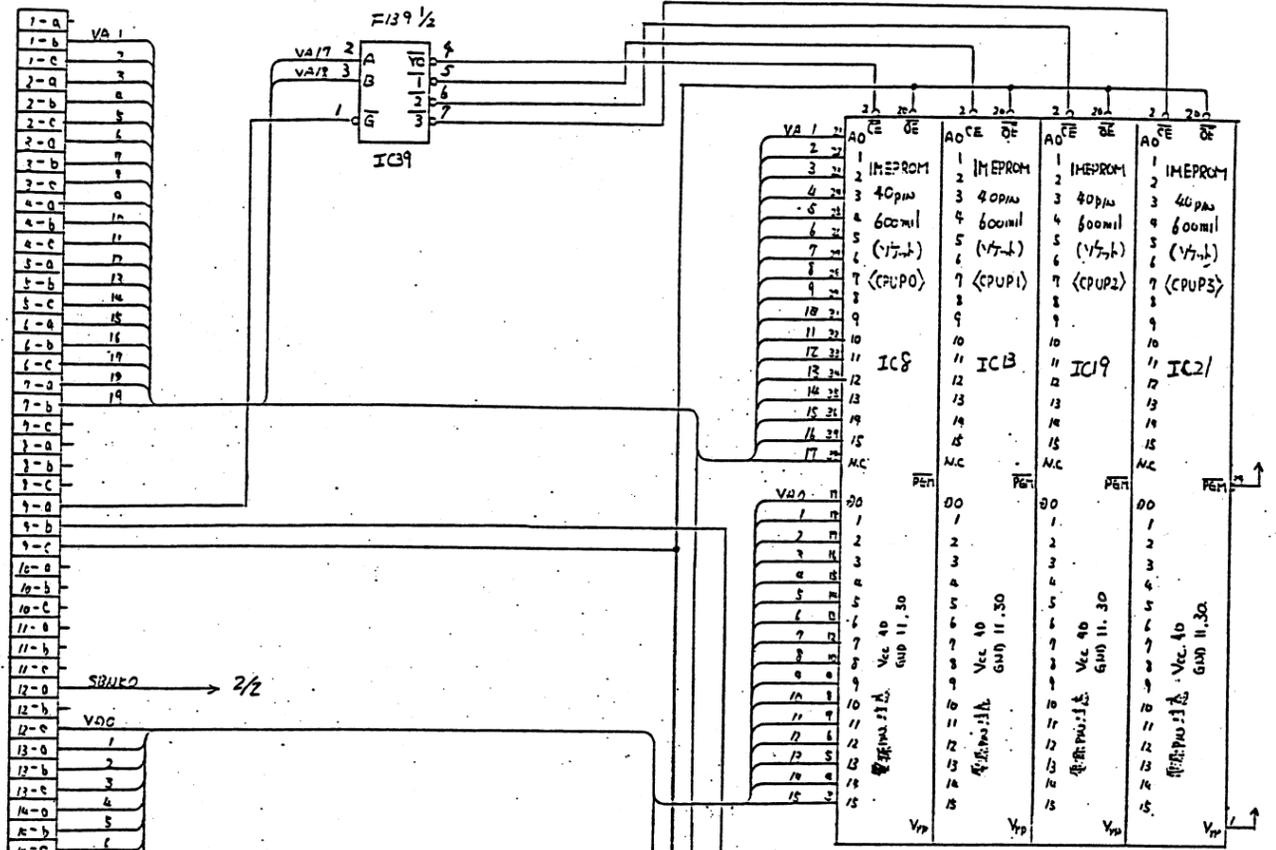
CN H
(DINコネクタ 96pin)



- 220μF x 4の基板1回隔=配置スリット。
- 線バネ短スリット。
- 各ICはソリッドパッド0.022μ (F45+L 917) を配する。
- 電源ラインは2層+1層極力長くはせず。
- JP SW1 ナルバネコネクタ=マトリクス接続。



CN E
(DINコネクタ 96pin)



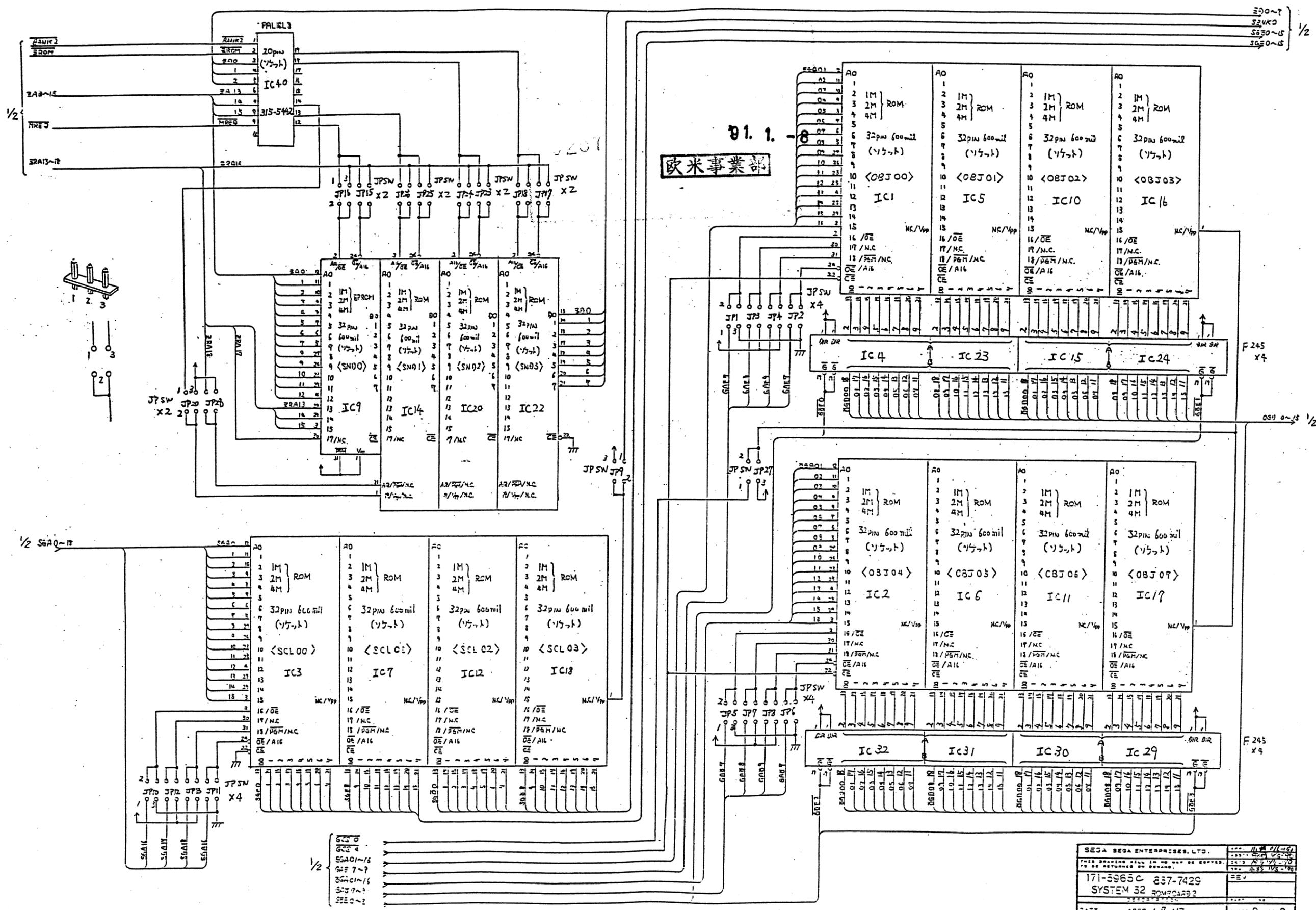
0257

91.1. - 8

欧米事業部

SEGA SEGA ENTERPRISES, LTD.		DATE	1980 / 7 / 12
171-5965C 857-7429		REV	1 of 2
SYSTEM 32 ROM BOARD 1			

WAS PIN No. 上原 1/6.70 中岡



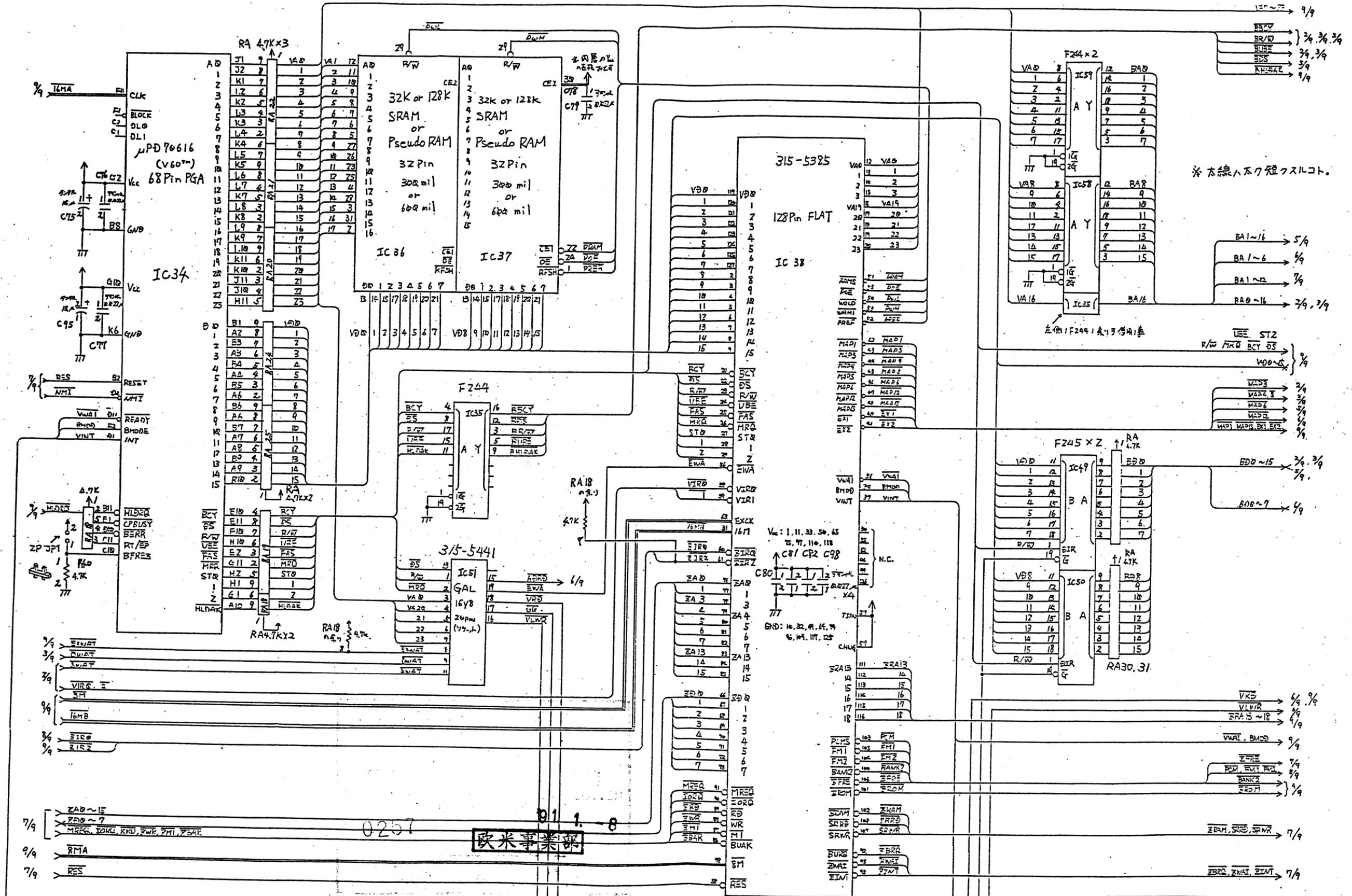
330~7
324K0
3230~15
3250~15

01.1 - 8
欧米事業部

F 245
x4

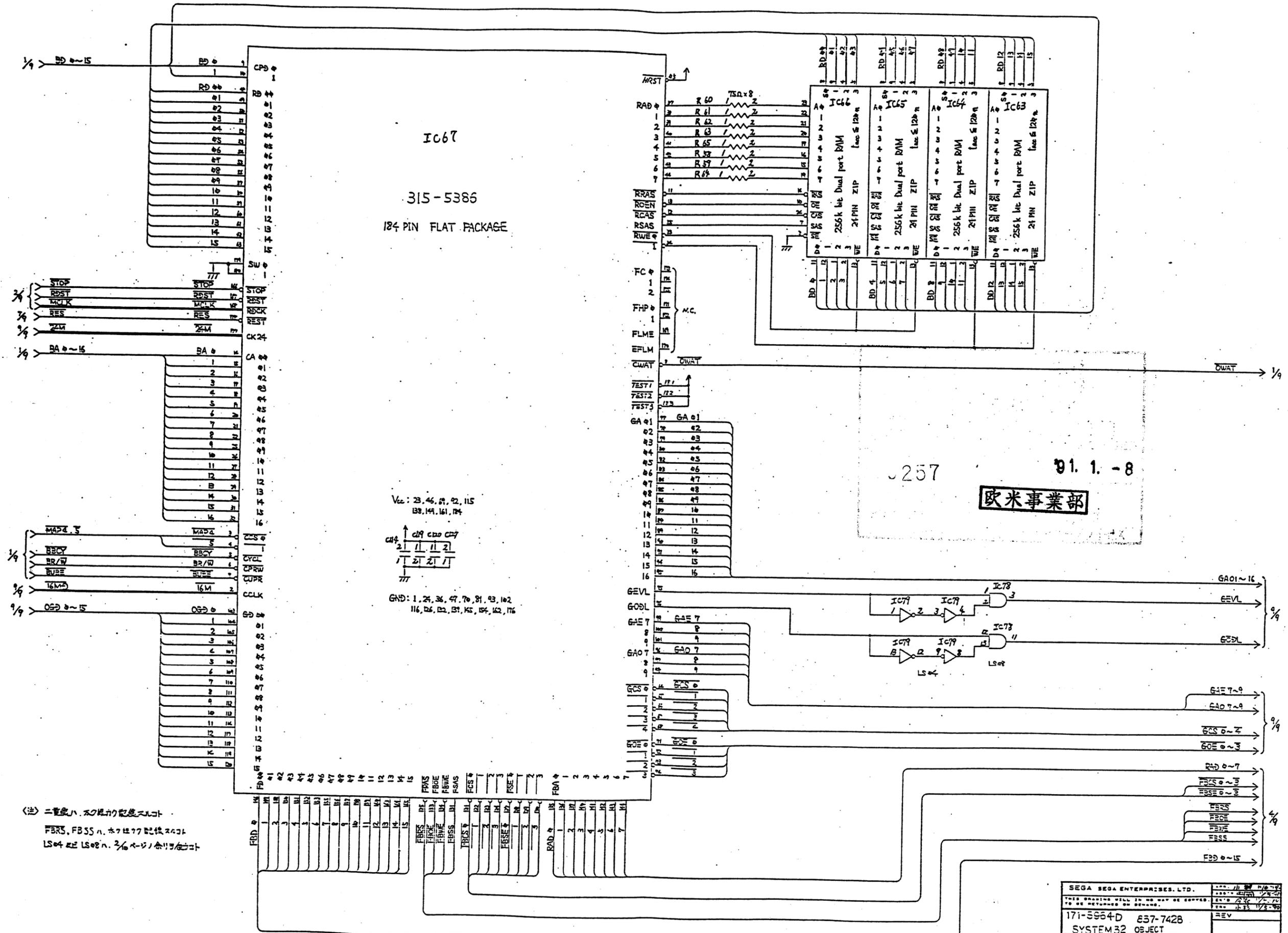
F 245
x4

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171-3365C 837-7429
SYSTEM 32 ROM BOARD
DATE 1990 / 7 / 12



0257 欧米事業部

SEGA SEGA ENTERPRISES, LTD.		REV
171-5964 D 857-7428		REV
SYSTEM 32 MAIN CPU		REV
DATE 1990 / 7 / 12		REV
		REV



IC67
315-5386
184 PIN FLAT PACKAGE

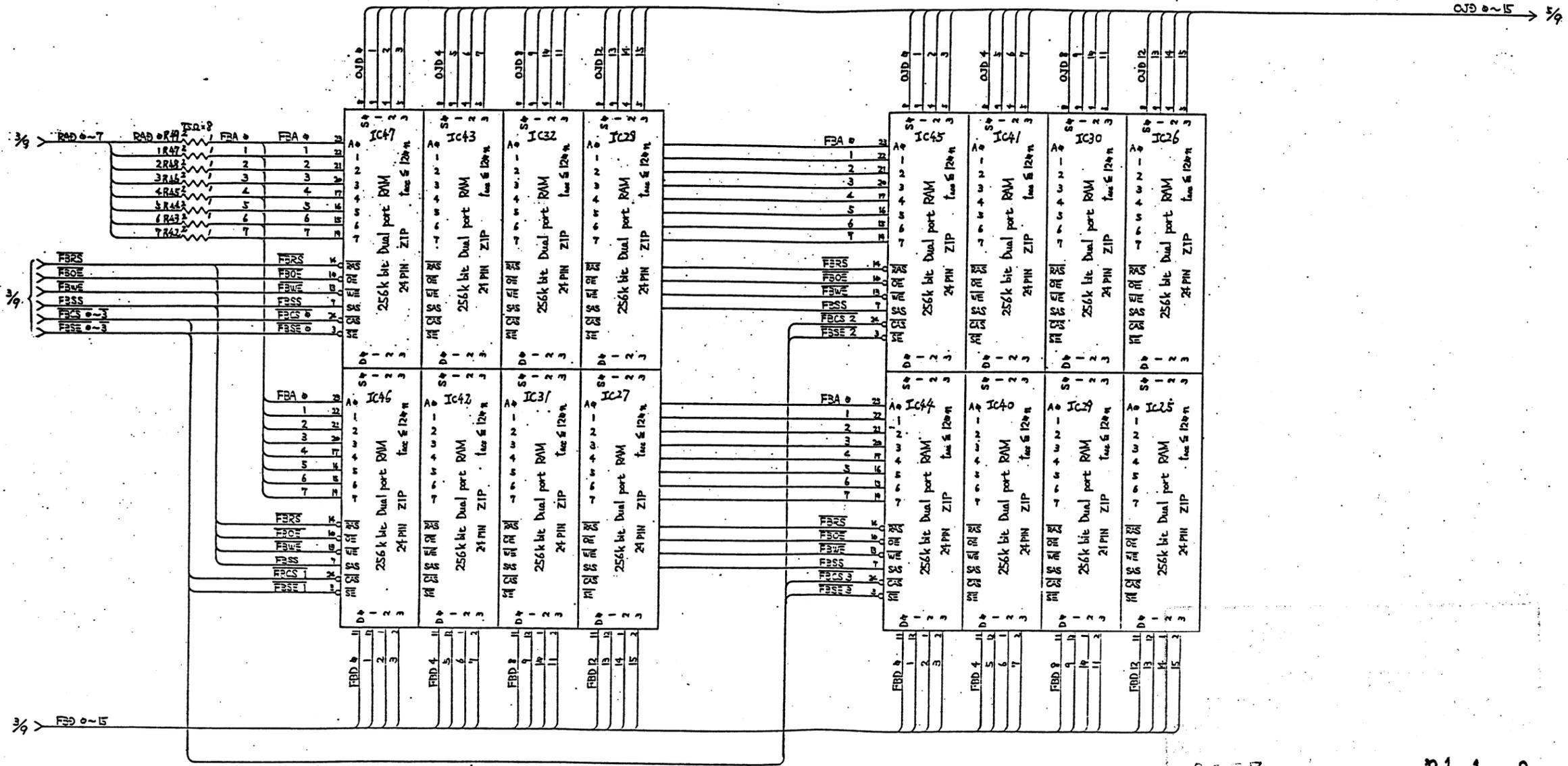
Vcc: 23, 46, 51, 52, 115
58, 144, 161, 174

GND: 1, 24, 36, 47, 70, 81, 83, 102
116, 126, 132, 137, 145, 154, 162, 176

257
91. 1. - 8
欧米事業部

(注) 二重線ハ、スロジック記号スロジック
FBSS, FBSSハ、ホフセツ記号スロジック
LS04 及 LS08ハ、3/4サイズ合ハシ合ハシ

SEGA SEGA ENTERPRISES, LTD.		REV
171-5964D 837-7428		REV
SYSTEM32 OBJECT		REV
DATE 1990/7/12	PAGE 3 of 9	



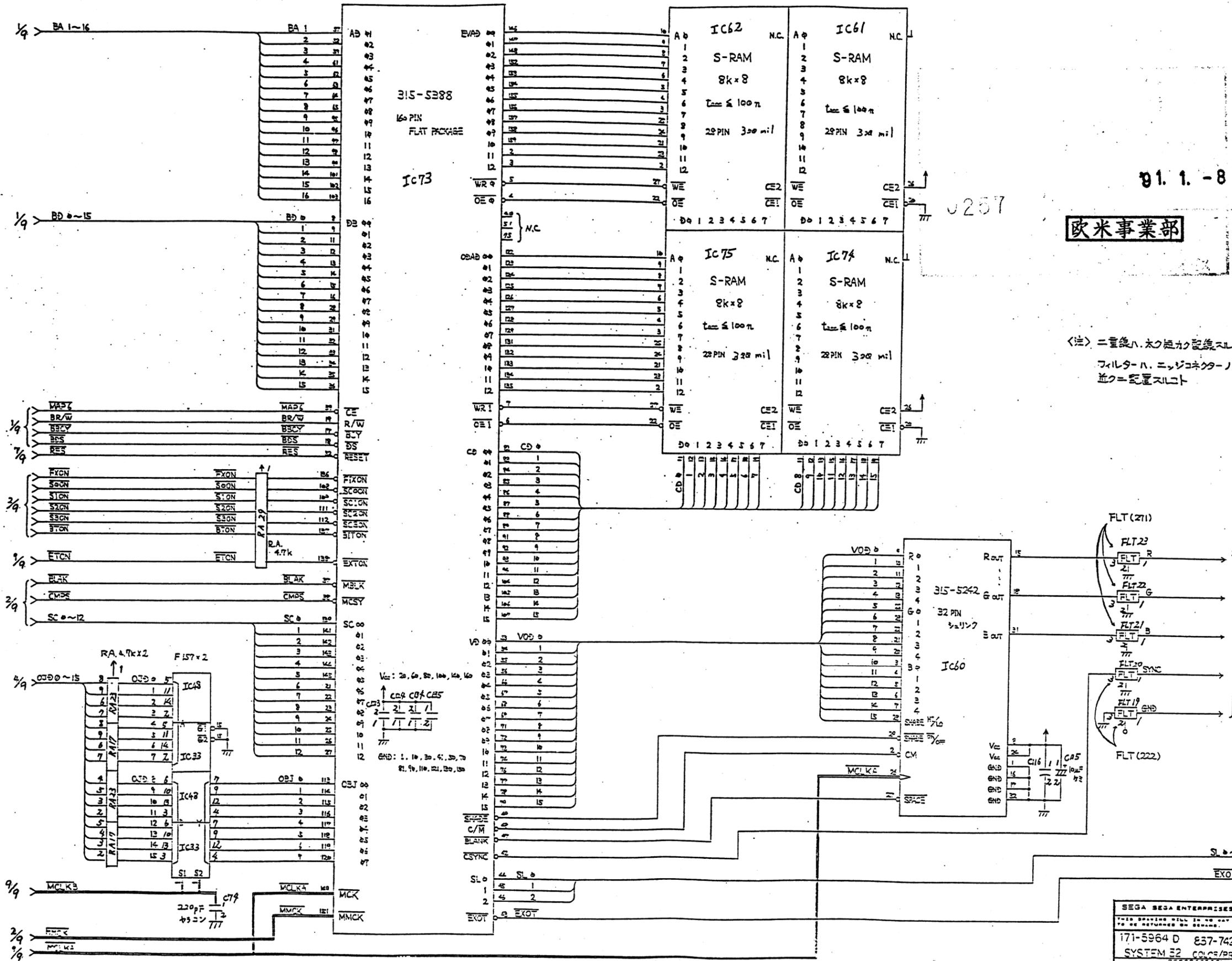
④ FBR5, FB55ハ、本7組カ7配ルスルコ

0257

01.1.-8

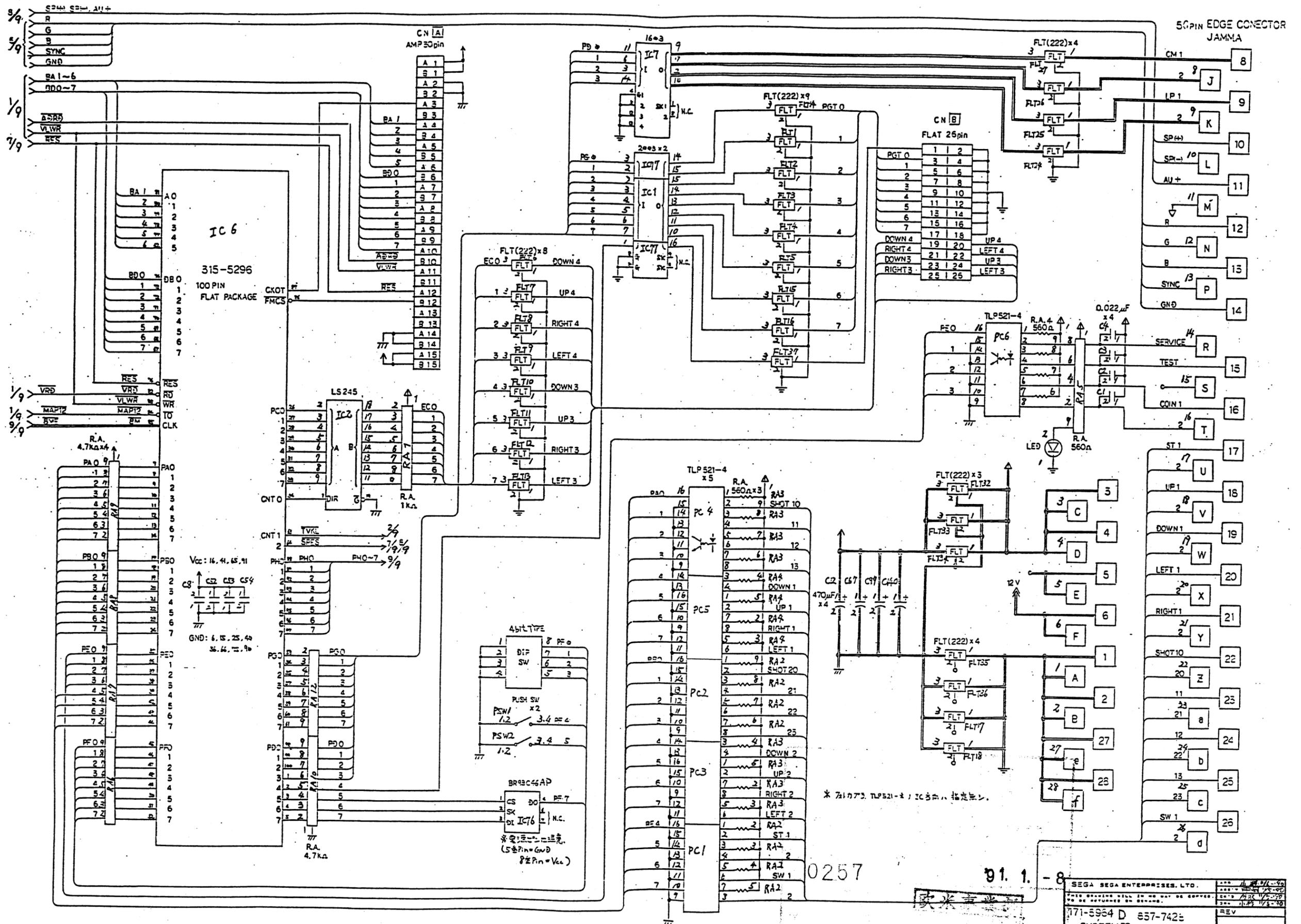
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171-5964 D 837-7428	REV
SYSTEM 32 FRAME MEMORY	REV
DATE 1990 / 7 / 12	SHEET 4 of 9

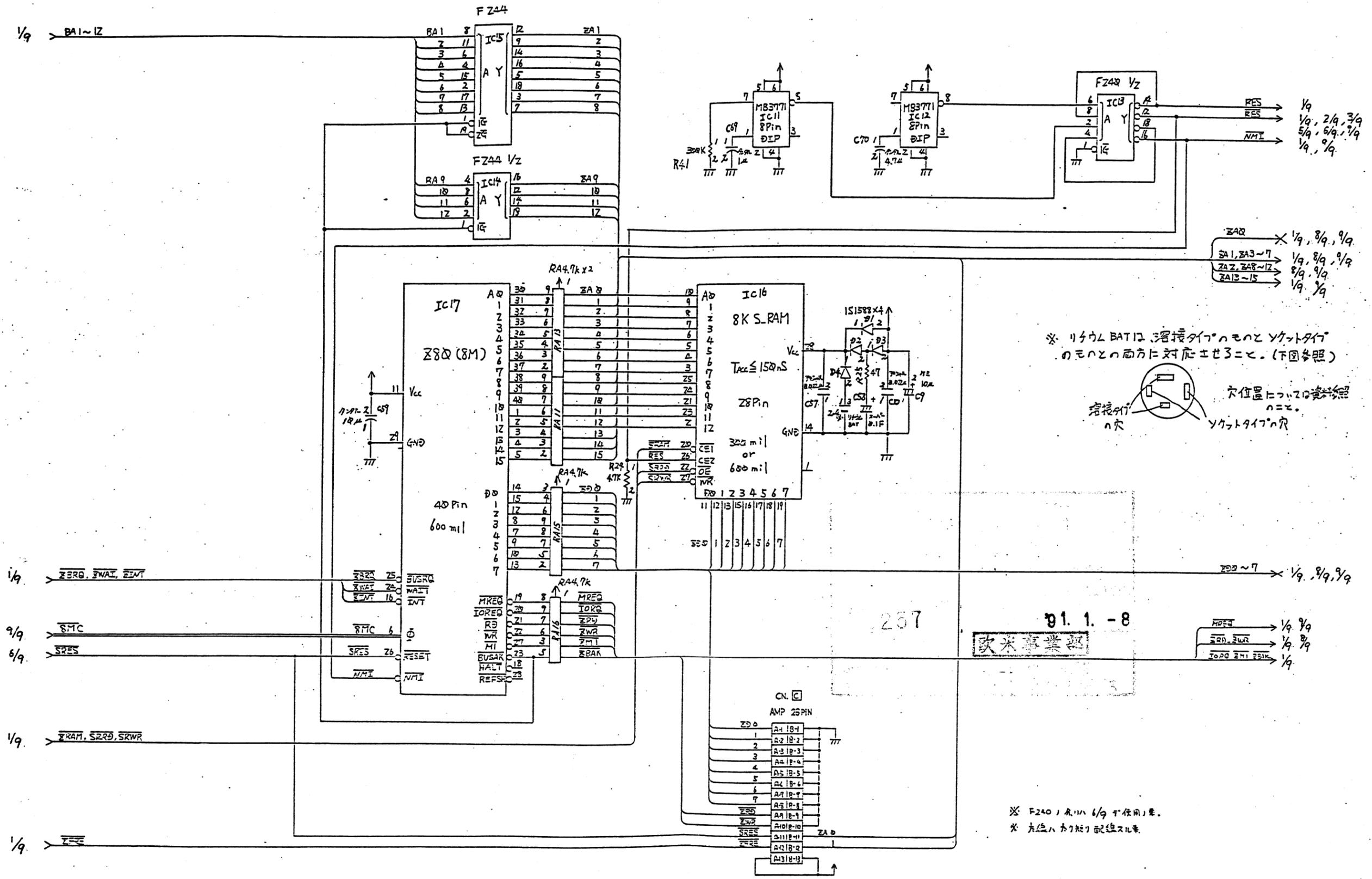


91.1.-8
欧米事業部

(注) ニ重線ハ、太ク短カ記録スロット
フィルタ-ハ、ニ、ジコネクタ-ノ、
途ク=配置スロット



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 91. 1. - 8
 * 710703, TLP521-4, IC5の仕様指定無し.



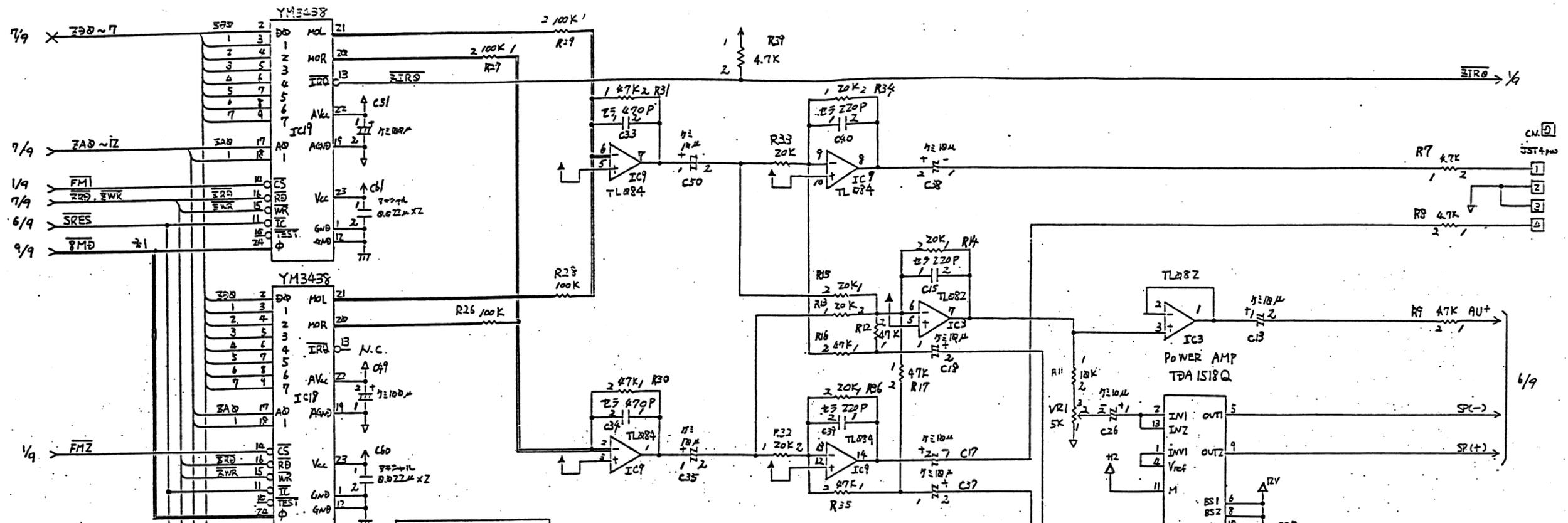
※ リチウム BATT は、溶接タイプのもので、ソケットタイプのものとの両方に対応させること。(下図参照)



91.1.-8
 欧米事業部
 MRES → 1/9 1/9
 ZD0, ZD1 → 1/9 1/9
 ZD2, ZD3 → 1/9 1/9

※ F244, A, Y, 1/9 使用注意。
 ※ 穴位置は、7 針の配線に注意。

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171-5964D 637-7428	SYSTEM 32 SOUND CHIP
DATE 1990 / 7 / 12	REV 7 09

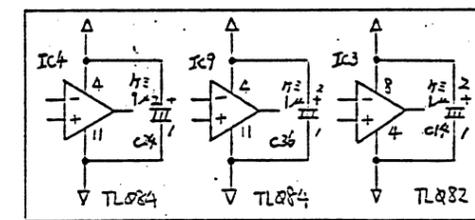


RF5C105		IC20		IC21		IC22		IC23	
Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	75	1	75	1	75	1	75	1	75
2	76	2	76	2	76	2	76	2	76
3	77	3	77	3	77	3	77	3	77
4	78	4	78	4	78	4	78	4	78
5	79	5	79	5	79	5	79	5	79
6	80	6	80	6	80	6	80	6	80
7	81	7	81	7	81	7	81	7	81
8	82	8	82	8	82	8	82	8	82
9	83	9	83	9	83	9	83	9	83
10	84	10	84	10	84	10	84	10	84
11	85	11	85	11	85	11	85	11	85
12	86	12	86	12	86	12	86	12	86
13	87	13	87	13	87	13	87	13	87
14	88	14	88	14	88	14	88	14	88
15	89	15	89	15	89	15	89	15	89
16	90	16	90	16	90	16	90	16	90
17	91	17	91	17	91	17	91	17	91
18	92	18	92	18	92	18	92	18	92
19	93	19	93	19	93	19	93	19	93
20	94	20	94	20	94	20	94	20	94
21	95	21	95	21	95	21	95	21	95
22	96	22	96	22	96	22	96	22	96
23	97	23	97	23	97	23	97	23	97
24	98	24	98	24	98	24	98	24	98
25	99	25	99	25	99	25	99	25	99
26	100	26	100	26	100	26	100	26	100

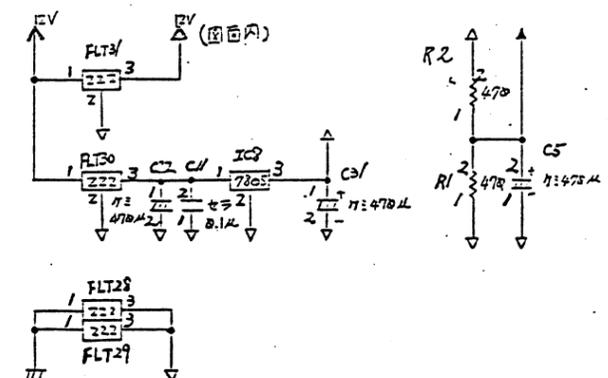
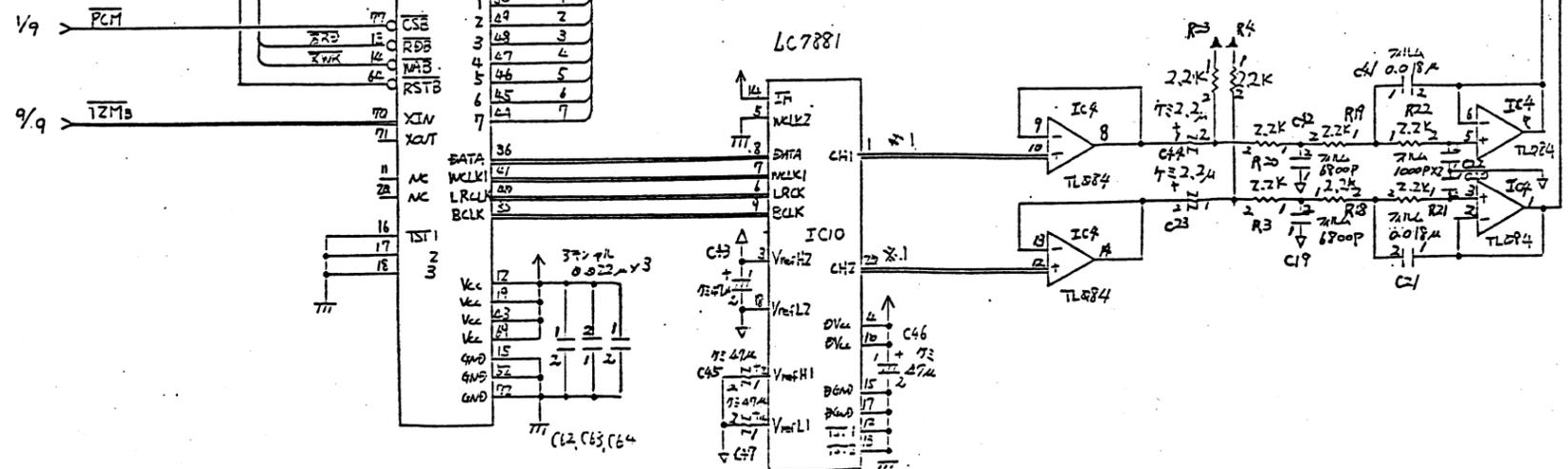
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91.1 - 8

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- *1. 二重線ハダク短クシロコ.
- *2. 4.1k AVcc (+5V), 4.1k AVcc (7.5V), 4.1k AVcc (+12V), 4.1k AGND
- *3. 7805 (二端子 L+L9-)ハ 基板ニ固定シル様ニシテ、基板ト接続シルハ GNDバタニシル事.



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SYSTEM52 FM/MS SOUND	REV 8

